ECE 223 LAB EXPERIMENT 4

Preparation Part:

a) Implement a XOR gate by only using inverter and NAND gates. Draw truth table and logic circuit.

b) Implement F= B'C'+BC+ABC by only using inverter and NAND gates. Draw truth table and logic circuit.

c) Implement a XOR gate by only using inverter and NOR gates. Draw truth table and logic circuit.

d) Implement 3 input AND gate by only using inverter and NOR gates. Draw truth table and logic circuit.

Experiment Part:

1) Set up the logic circuit a in preparation part and show the result to your instructor.

2) Set up the logic circuit d in preparation part and show the result to your instructor.